Dr. Michael Petter, Raphaela Palenta **Exercise Sheet 4**

Assignment 4.1 Memory Consistency

- 1. Given an execution path for each thread, what property does the hardware (or the model) have if only a single interleaving is possible?
 - strict consistency

sequential consistency

- weak consistency
- 2. What consistency guarantee does a system with a MESI cache but without store or invalidate buffers give?
 - strict consistencysequential consistencyweak consistency
- 3. A program reaching a state S (declared variables, values of variables, etc.) on weakly consistent hardware can always reach the same state S on sequentially consistent hardware.

Assignment 4.2 Semaphores, Locks, and Monitors

Are the following statements true or false?

- A semaphore can be used to implement a mutex.
 A mutex is always re-entrant.
 A monitor can be used as a mutex.
 Any deadlock-free program must acquire locks in a fixed order.
- 5. When acquiring locks in a fixed order to ensure deadlock-freedom, there is no advantage in releasing them in the opposite order.
- 6. The use of which concurrency construct may lead to starvation?
 - a wait-free algorithm
 - a lock-free algorithm
 - a lock where blocking threads are put into a queue
 - a signal-and-urgent-wait monitor where all waiting threads are tracked in queues

true false

7. Consider all program points p with the statement $lock(a_p)$ and a lock set L_p . Which statement is true?

The program is free of deadlocks if a_p is a lock and a_p ∈ L_p.
The program may have a deadlock if a_p is a lock and a_p ∈ L_p.
The program will deadlock if a_p is a lock and a_p ∈ L_p.
The program is free of deadlocks if a_p ∈ L_p implies that a_p is a monitor.

8. Consider the program P whose synchronization between its two threads is given by the following two program fragments. According to the definition of a deadlock

<pre>wait(A);</pre>	<pre>wait(B);</pre>			
if (rnd()) {	if (rnd()) {			
<pre>wait(B);</pre>	<pre>wait(C);</pre>			
if (rnd()) {	if (rnd ()) {			
<pre>wait(C);</pre>	<pre>wait(D); // compute</pre>			
// compute				
signal(C)	}			
}	}			
<pre>signal(B);</pre>	<pre>signal(B);</pre>			
}	<pre>signal(C);</pre>			
<pre>signal(A);</pre>	<pre>signal(D);</pre>			

P may deadlock. There exists a lock order between the locks.

P may deadlock. There exists no lock order between the locks.

P cannot deadlock. There exists a lock order between the locks.

P cannot deadlock. There exists no lock order between the locks.

- 9. By recording an interleaving of a program at runtime, we observe the following: Thread 1 releasing a lock A is descheduled and another Thread 2 is scheduled that then executes holding the same lock A.
 - This behavior should never happen since it violates the mutual exclusion property, so there must be an error in the program.
 - The lock is a signal-and-urgent-wait monitor.
 - The lock must be a signal-and-continue monitor.

Assignment 4.3 Deadlocks

Consider the following four functions:

				15	u() 1	24	V() 1
1	f() {	8	g() {	16	• • •	25	• • •
2	• • •	9		17	<pre>wait(B);</pre>	26	<pre>wait(C);</pre>
3	<pre>wait(A);</pre>	10	<pre>wait(A);</pre>	18	<pre>wait(C);</pre>	27	<pre>wait(B);</pre>
4	u();	11	v();	19		28	
5	<pre>signal(A);</pre>	12	<pre>signal(A);</pre>	20	<pre>signal(C);</pre>	29	<pre>signal(B);</pre>
6	• • •	13		21	<pre>signal(B);</pre>	30	<pre>signal(C);</pre>
7	}	14	}	22		31	• • •
				23	}	32	}

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--() [

1. Additionally, we are given a main function that runs **f** and **g** in parallel:

```
33 main() {
34 f(); || g();
35 }
```

Can this possibly cause a deadlock? If not, try to prove it using the *freedom of deadlock* theorem.

- 2. Assuming there is no possible deadlock, how can we change the main function in a simple way to render a deadlock possible?
- 3. Finally, we change the main function so that it runs f and g sequentially:

```
36 main() {
37 f();
38 g();
39 }
```

Obviously, no deadlock can occur (no parallelism and no lock is acquired multiple times without releasing it in between). Again try to prove this using the *freedom of deadlock* theorem.